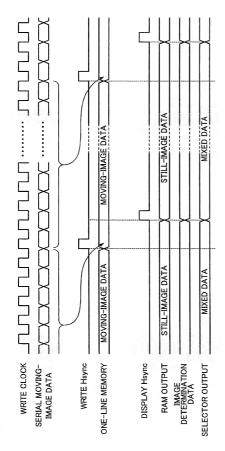


FIG. 3 **_28** STILL-IMAGE DATA -**60** -24 CONTROL SIGNALS -RAM CONTROL DISPLAY DATA RAM WRITE CLOCK -SHIFT REGISTER MOVING-IMAGE — DATA c26 WRITE Hsync · LINE MEMORY ĮΝ Ń √ _ 64 IMAGE DETERMINATION -DATA SELECTOR ĮΝ **66** DISPLAY Haync -**OUTPUT LATCH** łи ₇68 LIQUID CRYSTAL DRIVE

ĮΝ

FIG. 4



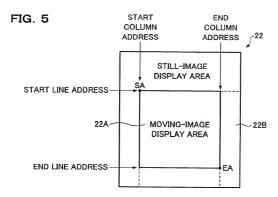


FIG. 6

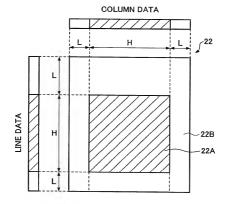


FIG. 7A

LINE DATA	COLUMN DATA	SELECTOR OUTPUT
Н	Н	MOVING-IMAGE DATA
Н	L	STILL-IMAGE DATA
L	Н	STILL-IMAGE DATA
L	L	STILL-IMAGE DATA

FIG. 7B

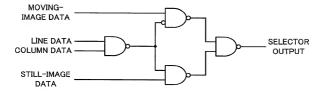
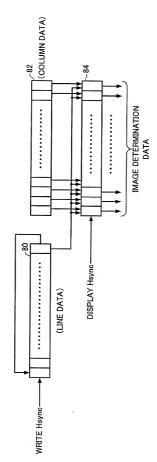
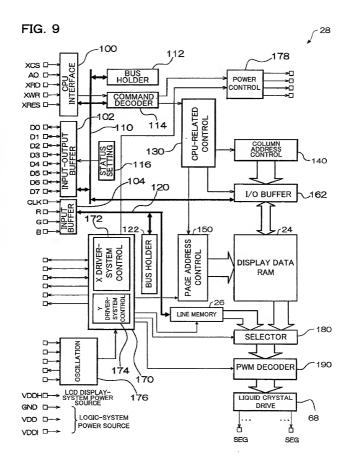


FIG. 8





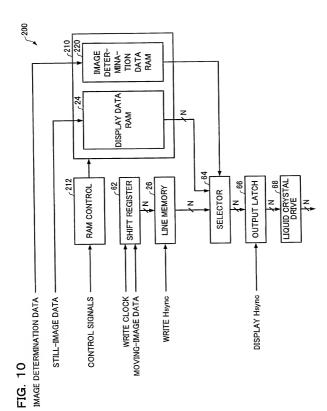


FIG. 11A



FIG. 11B



